

Notice of References Cited

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Applicant(s)/Patent Under
Reexamination
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Examiner
Justin P. Bettendorf

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-4,972,098	11-1990	Boudewijns, Arnoldus J. J.	327/566
	B	US-4,442,481	04-1984	Brahmbhatt, Dhaval J.	326/106
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)					
*					
	U	Schaumann, "Simulating Lossless Ladders with Transconductance-C Circuits," March 1998, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS-II: Analog and Digital Signal Processing, Vol. 45, No. 3.			
	V				
	W				
	X				

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

U.S. Patent and Trademark Office
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Notice of References Cited

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